| -4761    |        |   |   |                     |         |                  |
|----------|--------|---|---|---------------------|---------|------------------|
| Ref<br># | Hits   | Search Query  | DBs   | Default<br>Operator | Plurals | Time Stamp       |
| S1       | 16     | (("5103367") or ("5117121") or ("5325261") or ("5444597") or ("5452177") or ("5583736") or ("5810933") or ("5916689") or ("5916689") or ("6117246") or ("6149774") or ("6215643") or ("6236555") or ("6378600") or ("6388861")).PN. | USPAT;<br>USOCR   | OR                  | OFF     | 2005/02/04 09:52 |
| S2       | 6461   | electro\$1static adj3 chuck   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2004/10/22 11:22 |
| S3       | 874051 | clamp\$4  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2004/10/22 14:05 |
| S4       | 38661  | mem or<br>micro\$electro\$1mechanical   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2004/10/22 11:25 |
| S5       | 22     | (electro\$1static adj3 chuck) and clamp\$4 and (mem or micro\$electro\$1mechanical)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2004/10/22 10:36 |
| S6       | 71789  | (method or process) with (chuck<br>or clamp\$4)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2004/10/22 10:37 |
| S7       | 276340 | electro\$1static  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2004/10/22 14:05 |
| S8       | 4226   | ((method or process) with (chuck<br>or clamp\$4)) and electro\$1static  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2004/10/22 10:38 |
| S9       | 2609   | (((method or process) with (chuck<br>or clamp\$4)) and electro\$1static)<br>and (electro\$1static adj3 chuck)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR                  | ON      | 2004/10/22 10:38 |

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| S10 | 2972460 | semiconductor or si or silicon  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2004/10/22 17:15 |
| S11 | 2146    | ((((method or process) with (chuck or clamp\$4)) and electro\$1static) and (electro\$1static adj3 chuck)) and (semiconductor or si or silicon)                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON . | 2004/10/22 10:40 |
| S12 | 115965  | chuck or susceptor  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2004/10/22 14:03 |
| S13 | 8647    | electro\$1static and (chuck or susceptor)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2004/10/22 11:24 |
| S14 | 145     | (electro\$1static and (chuck or<br>susceptor)) and (mem or<br>micro\$electro\$1mechanical)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2004/10/22 11:24 |
| S15 | 57413   | mem or<br>micro\$electro\$1mechanical or<br>micro\$1mechanical\$2 or<br>micro\$1machin\$4   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2005/02/04 09:51 |
| S16 | 408     | (electro\$1static and (chuck or<br>susceptor)) and (mem or<br>micro\$electro\$1mechanical or<br>micro\$1mechanical\$2 or<br>micro\$1machin\$4)                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2004/10/22 11:26 |
| S17 | 100     | ((electro\$1static and (chuck or<br>susceptor)) and (mem or<br>micro\$electro\$1mechanical or<br>micro\$1mechanical\$2 or<br>micro\$1machin\$4)) and clamp\$4 | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2004/10/22 11:42 |
| S18 | 4924    | (438/758 or 438/761 or 438/763 or 438/778 or 438/800).ccls.   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2004/10/22 11:46 |
| S19 | 502     | ((438/758 or 438/761 or 438/763 or 438/778 or 438/800).ccls.) and (chuck or susceptor)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON   | 2004/10/22 11:46 |

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| S20 | 10     | (((438/758 or 438/761 or 438/763 or 438/778 or 438/800).ccls.) and (chuck or susceptor)) and (mem or micro\$electro\$1mechanical or micro\$1mechanical\$2 or micro\$1machin\$4)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 11:46 |
| S21 | 29122  | (multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:01 |
| S22 | 137322 | multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:03 |
| S23 | 162031 | ((multi or multipl\$6 or bi or dual<br>or double) adj2 (pole or polar\$5))<br>or (multipol\$6 or bipol\$6 or<br>dualpol\$6 or doublepol\$6) or<br>(multiplepol\$6)   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 17:17 |
| S24 | 184113 | chuck or susceptor or ((wafer or<br>substrate) near4 hold\$4)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:04 |
| S25 | 280919 | (chuck or susceptor or ((wafer or<br>substrate) near4 hold\$4)) or<br>platen   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:05 |
| S26 | 3773   | (((multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)) or (multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6) or (multiplepol\$6)) and ((chuck or susceptor or ((wafer or substrate) near4 hold\$4)) or platen)                | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:05 |
| S27 | 874051 | clamp\$4   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:05 |
| S28 | 895    | ((((multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)) or (multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6) or (multiplepol\$6)) and ((chuck or susceptor or ((wafer or substrate) near4 hold\$4)) or platen)) and clamp\$4 | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:05 |

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| S29      | 276340 | electro\$1static   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:06 |
| S30      | 435    | (((((multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)) or (multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6) or (multiplepol\$6)) and ((chuck or susceptor or ((wafer or substrate) near4 hold\$4)) or platen)) and clamp\$4 ) and electro\$1static   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:06 |
| S31      | 136328 | mem or<br>micro\$electro\$1mechanical or<br>micro\$1mechanical\$2 or<br>micro\$1machin\$4 or<br>electro\$1mechanical\$2  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:07 |
| S32      | 84     | ((((((multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)) or (multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6) or (multiplepol\$6)) and ((chuck or susceptor or ((wafer or substrate) near4 hold\$4)) or platen)) and clamp\$4 ) and electro\$1static ) and (mem or micro\$electro\$1mechanical or micro\$1mechanical\$2 or micro\$1machin\$4 or electro\$1mechanical\$2)  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:31 |
| S33      | 351    | ((((((multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)) or (multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6) or (multiplepol\$6)) and ((chuck or susceptor or ((wafer or substrate) near4 hold\$4)) or platen)) and clamp\$4 ) and electro\$1static ) not ((((((multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)) or (multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6) or (multiplepol\$6)) and ((chuck or susceptor or ((wafer or substrate) near4 hold\$4)) or platen)) and clamp\$4 ) and electro\$1static ) and (mem or micro\$electro\$1mechanical or micro\$1mechanical\$2 or micro\$1mechanical\$2 or electro\$1mechanical\$2)) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2004/10/22 14:32 |

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| S34 | 2055579 | protru\$6 or stud\$4 or island or pedestal or column                          | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 14:33 |
| S35 | 199     | ((((((((((((((((((((((((((((((((((((((  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 15:16 |
| S36 | 2       | ("5572398").PN.   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | OFF | 2004/10/22 15:26 |
| S37 | 2972460 | semiconductor or si or silicon  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 17:15 |
| S38 | 31118   | (semiconductor or si or silicon) adj3 (platform or base)                      | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 17:16 |
| S39 | 1352    | ((semiconductor or si or silicon)<br>adj3 (platform or base)) and<br>clamp\$4 | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 17:17 |

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| S40 | 283   | (((semiconductor or si or silicon)<br>adj3 (platform or base)) and<br>clamp\$4 ) and (chuck or susceptor<br>or ((wafer or substrate) near4<br>hold\$4))   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 17:21 |
| S41 | 29    | ((((semiconductor or si or silicon) adj3 (platform or base)) and clamp\$4 ) and (chuck or susceptor or ((wafer or substrate) near4 hold\$4))) and (((multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)) or (multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6))  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 17:17 |
| S42 | 93    | ((((semiconductor or si or silicon)<br>adj3 (platform or base)) and<br>clamp\$4 ) and (chuck or susceptor<br>or ((wafer or substrate) near4<br>hold\$4))) and electro\$1static  | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 17:22 |
| S43 | 78    | (((((semiconductor or si or silicon) adj3 (platform or base)) and clamp\$4 ) and (chuck or susceptor or ((wafer or substrate) near4 hold\$4))) and electro\$1static ) not ((((semiconductor or si or silicon) adj3 (platform or base)) and clamp\$4 ) and (chuck or susceptor or ((wafer or substrate) near4 hold\$4))) and (((multi or multipl\$6 or bi or dual or double) adj2 (pole or polar\$5)) or (multipol\$6 or bipol\$6 or dualpol\$6 or doublepol\$6))) | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2004/10/22 17:22 |
| S48 | 61691 | mem or<br>micro\$electro\$1mechanical or<br>micro\$1mechanical\$2 or<br>micro\$1machin\$4   | US-PGPUB;<br>USPAT;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB           | OR | ON  | 2005/02/04 09:51 |
| S49 | 16    | (("5103367") or ("5117121") or ("5325261") or ("5444597") or ("5452177") or ("5583736") or ("5810933") or ("5916689") or ("5958813") or ("6117246") or ("6149774") or ("6215643") or ("6236555") or ("6378600") or ("6388861")).PN.   | USPAT;<br>USOCR   | OR | OFF | 2005/02/04 09:52 |
| S50 | 1     | S48 and S49   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON  | 2005/02/04 09:53 |